SAMUEL GONSHAW						
•	sam@gonshaw.com 🔀 gonshaw.com 🗈 samgonshaw 🖓 sjg10					
RECENT EMPLOYMENT	<ul> <li>BIOS Health Ltd.</li> <li>Medical device startup developing AI-powered embedded neural interfaces.</li> <li>Embedded Systems Engineer, Cambridge, UK 2018–</li> <li>Architecture of embedded data collection systems driving 25+ live medical trials.</li> <li>Scoping, onboarding and managing edge systems for multisite deployment.</li> <li>Development of C++/Qt desktop applications providing clinical interfaces.</li> <li>Compiled ISO 14971 risk register to provide accountability and product safety.</li> <li>Development of an IEC 62304 Class C device unlocking clinical device use.</li> <li>Full lifecycle development of FPGA solutions for highly accurate data collection.</li> <li>Integration with cloud hosted Python ML pipelines unlocking novel data insights.</li> <li>Authoring software lifecycle processes and implementing supporting systems used to drive productivity and project management across teams in a new company.</li> </ul>					
	Mathematics Tutor       2022–2023         • Tutoring of GCSE and A-Level Mathematics on a 1–1 basis.       2022–2023					
	Toshiba America Storage Research Design Centre / OCZ Storage SolutionsMultinational developing SSDs for Consumer, Datacentre and Enterprise applications.Senior Firmware Engineer, Oxford, UK / San Jose, CA2015–2018Firmware Engineer, Oxford, UK2014–2015• Design of bare metal C firmware for enterprise SSDs released to market.• Design of ASIC verification firmware eliminating respin costs.• Development of hardware abstraction libraries saving development costs.• New feature development for old products revitalising sales.• Linux driver and test tooling development improving device reliability.• Lifetime product support for released products improving customer experience.• Designing and performing characterisation experiments on flash technologies.• Liaising between geographically diverse teams on multidisciplinary development.					
	Imperial College LondonResearch Assistant2012–2015• Individual research converting Masters thesis into publishable content.• Python/MAGMA package creation for the Open Source SAGEMath project.• Multi-threaded C application for weighted projective space characterisation.					
EDUCATION	Imperial College London2010–2014• MSci. incorporating BSc. ARCS 1 <sup>st</sup> Class (Hons.) Mathematics					
	Birkat Moshe Talmudic Seminary2007• Jewish theology with emphasis on Talmudic texts.					
PUBLICATIONS	<ul> <li>Online bayesian optimization of vagus nerve stimulation.</li> <li>Lorenz Wernisch et al. J. Neural Eng., 21(2):026019, April 2024</li> <li>Using neural biomarkers to personalize dosing of vagus nerve stimulation.</li> <li>Antonin Berthon et al. bioRxiv, 2023. doi: 10.1101/2023.08.30.555487</li> <li>Unipotent class representatives for finite classical groups.</li> <li>S. Gonshaw, M. Liebeck, and E.A. O'Brien. J. of Group Theory, 20(3):505–525, 2017</li> <li>Further publications: https://orcid.org/0009-0009-4376-2711</li> </ul>					

PROGRAMMING	Embedded					
AND TOOLING	С	Bare	Metal/Multi-threaded Development	Flue	$\operatorname{ent}$	
	ARM, Xtensa ASM	Acceleration of Critical Paths		Intermedia	ite	
	fHDL	Pythe	on Generated FPGA Development	Intermedia	ite	
	Verilog, Vivado, Libero	FPGA	A Development and TCL Scripting	Intermedia	ite	
	Applications					
	C++	C++2	17/STL App and Driver Development	Flue	$\operatorname{ent}$	
	Python	Script	ing and Scientific Computation	Flue	$\operatorname{ent}$	
	Configuration	YAM	L, XML, JSON	Intermedia	ite	
	$\mathrm{Qt}/\mathrm{QML}$	C++	Integrated UI Development	Famili	iar	
	Build and Test					
	GTest	Unit '	Test Development	Flue	$\operatorname{ent}$	
	Make, CMake	Build	management	Flue	$\operatorname{ent}$	
	Analysis/Debug	GDB,	Valgrind, Prof, clang-format	Intermedia	ite	
	Docker	Build	environment management	Intermedia	ate	
	Modelsim,Verilator Platforms	Wave	debugging for FPGA/ASIC	Intermedia	ite	
	Linux	Modu	le Development, Bash-fu, Tool Packaging	Flue	$\operatorname{ent}$	
	AWS	S3, C	loudfront, Route53, Lambda	Famili	iar	
	Technologies					
	High Speed Buses	PCIe/	NVMe, SATA, AMBA, USB, FT245	Intermedia	ite	
	Low Speed Buses	I2C, S	SPI, LVDS, JTAG, UART	Intermedia	ite	
	Embedded Systems	Jetsor	n, Arduino, STM32, RPi, Rock64	Intermedia	ite	
	Project Management					
	Gitlab, Atlassian	Issue	Tracking and Planning	Flue	$\operatorname{ent}$	
	Gitlab CI, Jenkins	CI/CI	D Frameworks	Flue	$\operatorname{ent}$	
	Git, SVN Other	Versio	on Control Systems	Flue	$\operatorname{ent}$	
	Mathematical	SACE	E/MAGMA/GAP/MATLAB/Maple/LAT <sub>F</sub>	X Famili	ior	
	Java/Kotlin		bid Development	Hobby		
	Home Automation		MQTT, ESPHome	Hobby		
	Web	,	P/JS/CSS	Hobby		
	Angular		end web	Hobby		
	-	FIOID		Ū.		
OTHER	Advent of Code		Company Leaderboard Winner	2021 - 2		
EXPERIENCE	Imperial College Lor		Undergraduate Mathematics Tutor	2013 - 2		
	Westminster City Sc		KS4-KS5 Teaching Assistant		2013	
	Imperial College Lor	ndon	Colloquium Founder & Speaker	2012 - 2		
	Mangahigh.com		Educational Content Author	2010-2		
	Israeli Defence Force		HR Coordinator / Infantry	2008 - 2		
	Lewis and Co. Solici		General Office Assistant	2008, 2		
	Jerusalem Foundatio	n	Writer Internship		2008	
	Livnot U'Lehibanot		Painter/Decorator/Handyman	2	2007	
AWARDS	Mar 2014: ICHACK Best E-learning Application, Most Monetizable Concept Sep 2010: Outstanding Soldier of 933rd Training Base					
OTHER SKILLS		-	sh and Modern Hebrew.			
	Agile: Scrum Open Developer Qualification					